

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#34

John S. Yates, Jr., et al.

155

Serial No.:

09/429,094

Art Unit:

2155

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Filed:

October 28, 1999

Examiner:

David Eng

Title:

SIDE TABLES ANNOTATING AN INSTRUCTION STREAM

RECEIVED

INFORMATION DISCLOSURE STATEMENT

MAR 2 6 2003

COMMISSIONER FOR PATENTS Washington, D.C. 20231

Technology Center 2100

In accordance with 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant wishes to make of record the enclosed items, as listed on the accompanying Form PTO-1449. Applicant respectfully requests the Examiner to fully consider the items and independently ascertain their teaching before issuance of the next action, and to make them of record in the file. The Examiner is also requested to initial and return a copy of the enclosed Form PTO-1449 to evidence such consideration.

- 1. Applicant has listed publication dates on the attached Form PTO-1449 based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated. Applicant reserves the right to establish the patentability of the claims over any information provided herewith, and/or to prove that this information may not be enabling for the teachings purportedly offered. This Information Disclosure Statement should not be construed as a representation that information more material to the examination of this application does not exist.
- 2. For any item listed on the enclosed copy of Form PTO-1449 for which a copy is not enclosed with this Information Disclosure Statement and not already made of record in this application, a copy was previously cited by or submitted to the Patent and Trademark Office in application Serial No. 09/239,194, filed January 28, 1999, Yates et al., Executing Programs for a First Computer Architecture on a Computer of a Second Architecture, in application Serial No. 09/322,443, filed May 28, 1999, Reese et al., Profiling of Computer Programs Executing in Virtual Memory Systems, or in application Serial No. 09/385,394, filed August 30, 1999, Yates et al., Computer for Executing Two Different Instruction Sets.

I certify that this correspondence, along with any documents referred to therein, is being deposited with the United States Postal Service on March 17, 2003 as First Class Mail in an envelope with sufficient postage addressed to The Commissioner for Patents, Box RCE, Washington D.C. 20231.

09/429.094

- 3. This Information Disclosure Statement is being filed to the knowledge of the undersigned, before the mailing date of a first Office Action on the merits after filing of a request for continued examination filed herewith on March 17, 2003. No fee is due, pursuant to 37 C.F.R. §1.97(b)(4).
- 4. The Commissioner is hereby authorized to charge any additional fees that may be required for this Information Disclosure Statement, or credit any overpayment, to Deposit Account 50-0675, Order No. 5231.16-4004C.

Respectfully submitted,

SCHULTE ROTH & ZABEL, LLP

Dated: March 17, 2003

Registration No. 36,461

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FORM PTO-1449 U.S. DEPARTA OF COMMERCE PATENT AND TRADEMARK OFFICE				5231.16-4004C		1	serial no. 09/429,094	
				APPLICANT				
INFORMATION DISCLOSURE CITATION				John S. Yates, Jr., et al.				
(Use several sheets if necessary)				FILING DATE October 28, 1999		GROUP ART UNIT 2155		
		U.S. PATI	ENT	DOCUMENTS		12133		
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INITIAL	4,779,187	10/18/1988	Letwin Larsen Murao et al.		717	2.201		
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